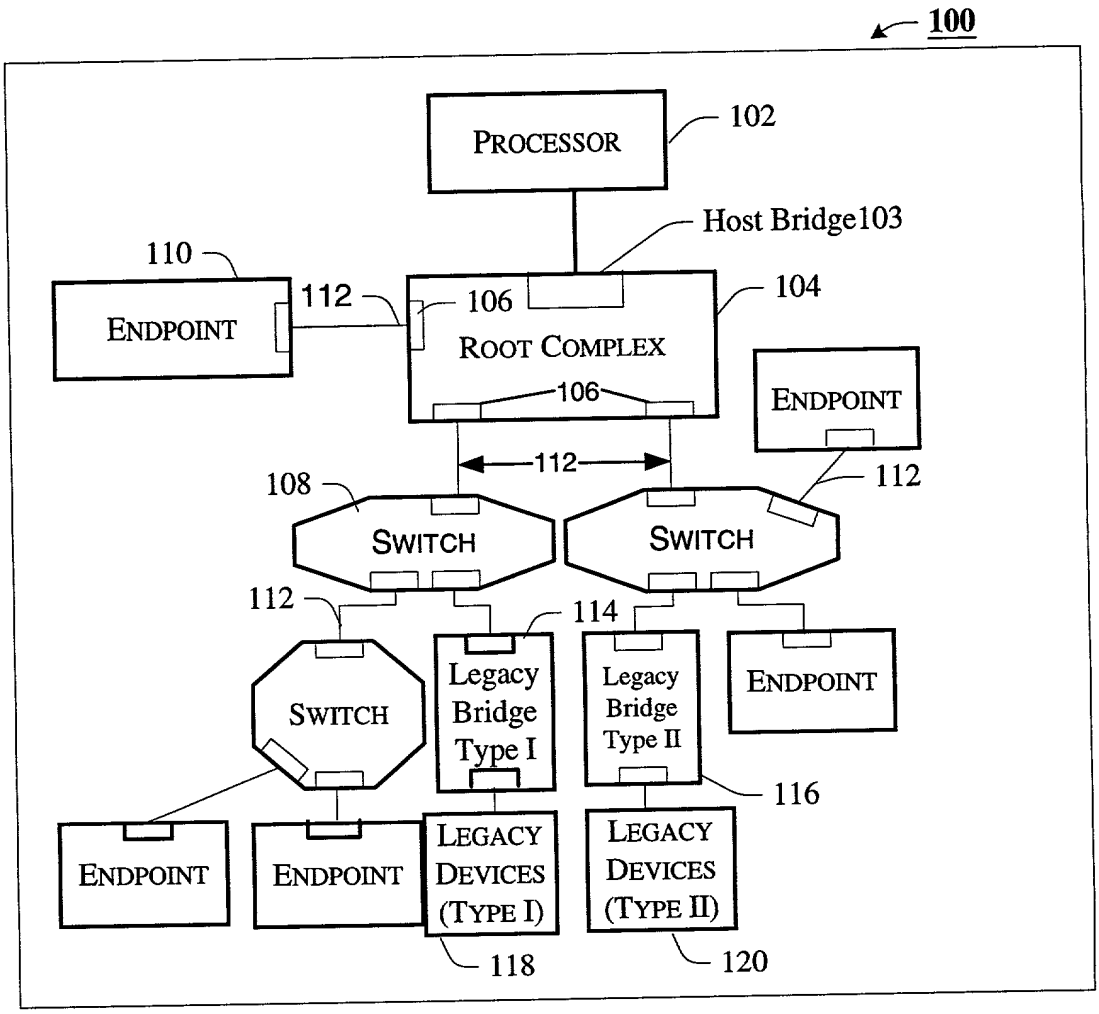
100
PROCESSOR
102
Host Bridge103
110
ENDPOINT
112
106
ROOT COMPLEX
104
106
ENDPOINT
112
112
108
SWITCH
SWITCH
112
112
114
SWITCH
Legacy Bridge Type I
Legacy Bridge Type II
ENDPOINT
116
ENDPOINT
ENDPOINT
LEGACY DEVICES (TYPE I)
LEGACY DEVICES (TYPE II)
118
120

FIGURE 1

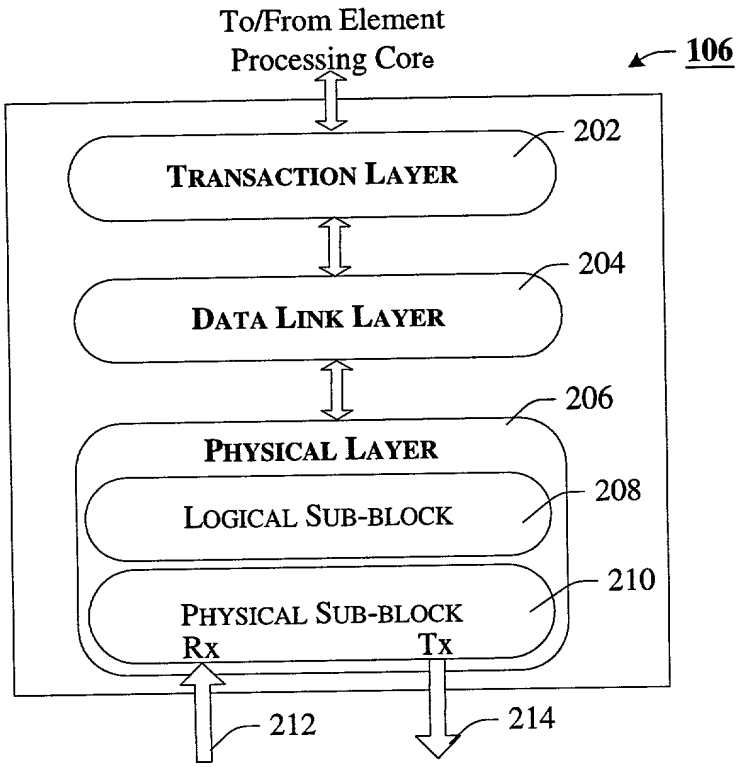
To/From Element
Processing Core
106
202
TRANSACTION LAYER
204
DATA LINK LAYER
206
PHYSICAL LAYER
208
LOGICAL SUB-BLOCK
210
PHYSICAL SUB-BLOCK
Rx
Tx
212
214

FIGURE 2

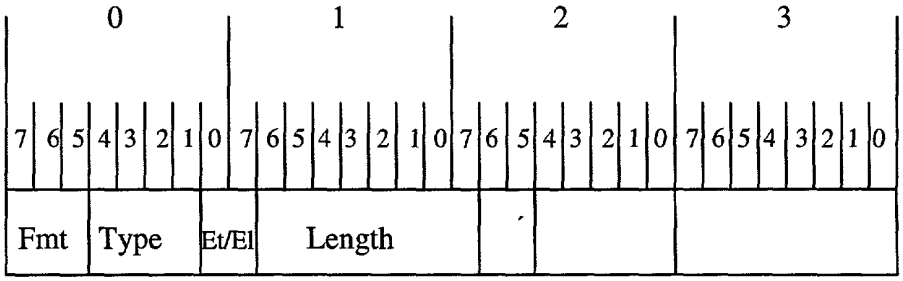

Figure 3

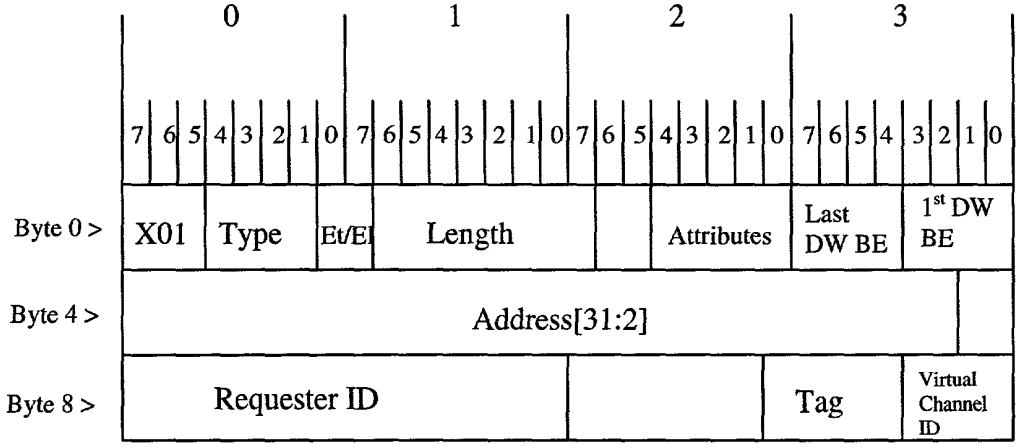

Figure 4

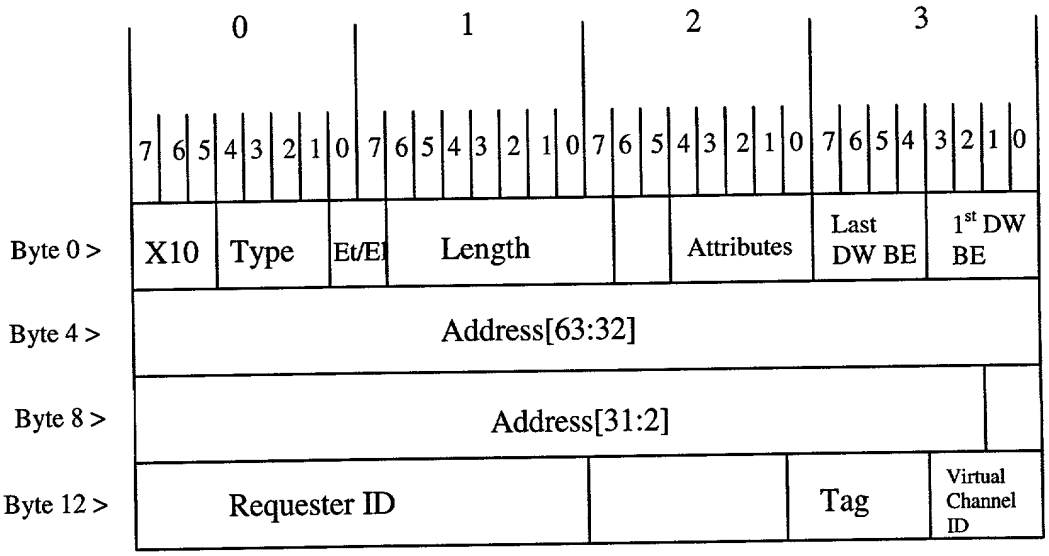

Figure 5

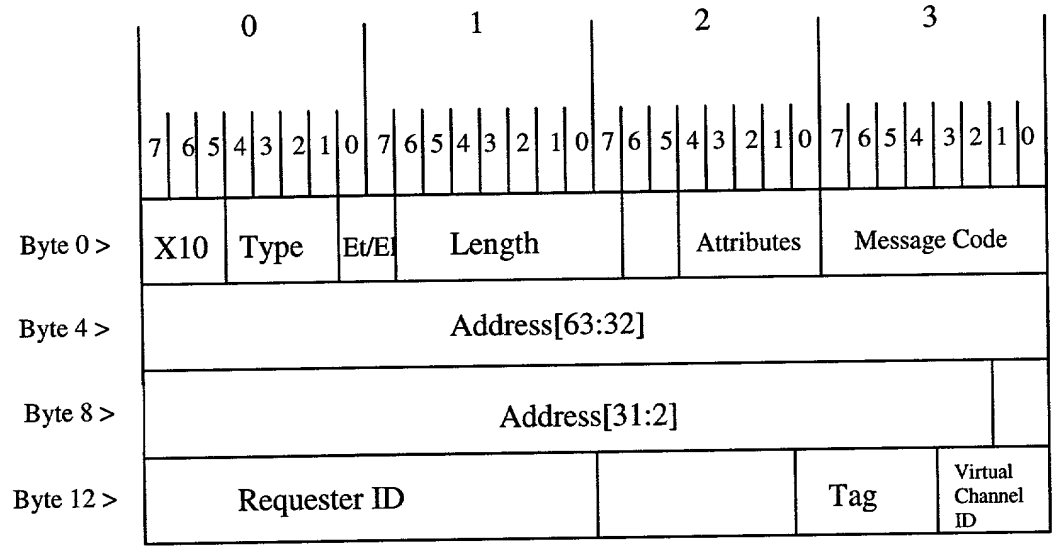

Figure 6

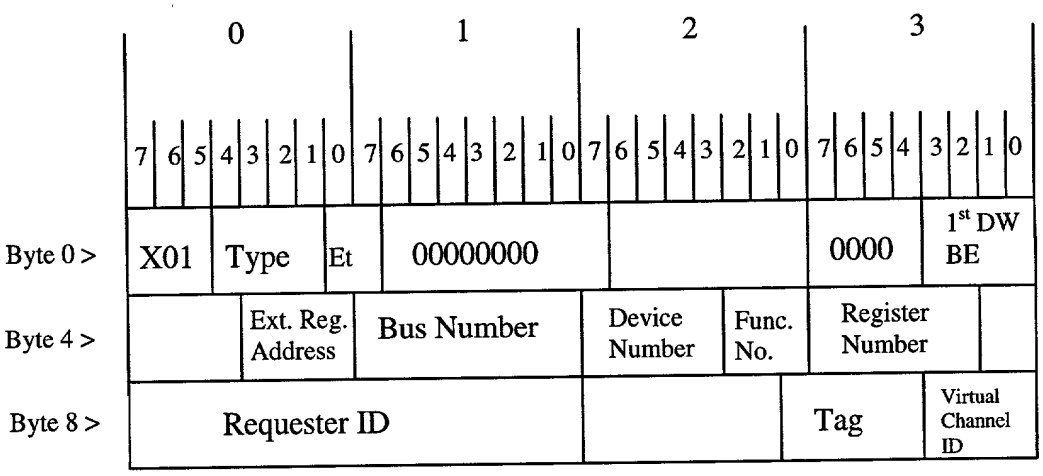

Figure 7

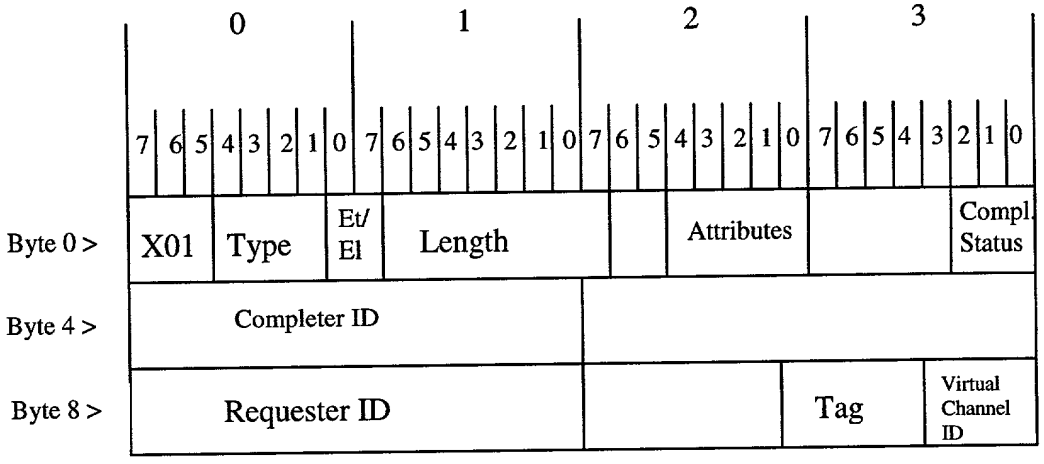

Figure 8

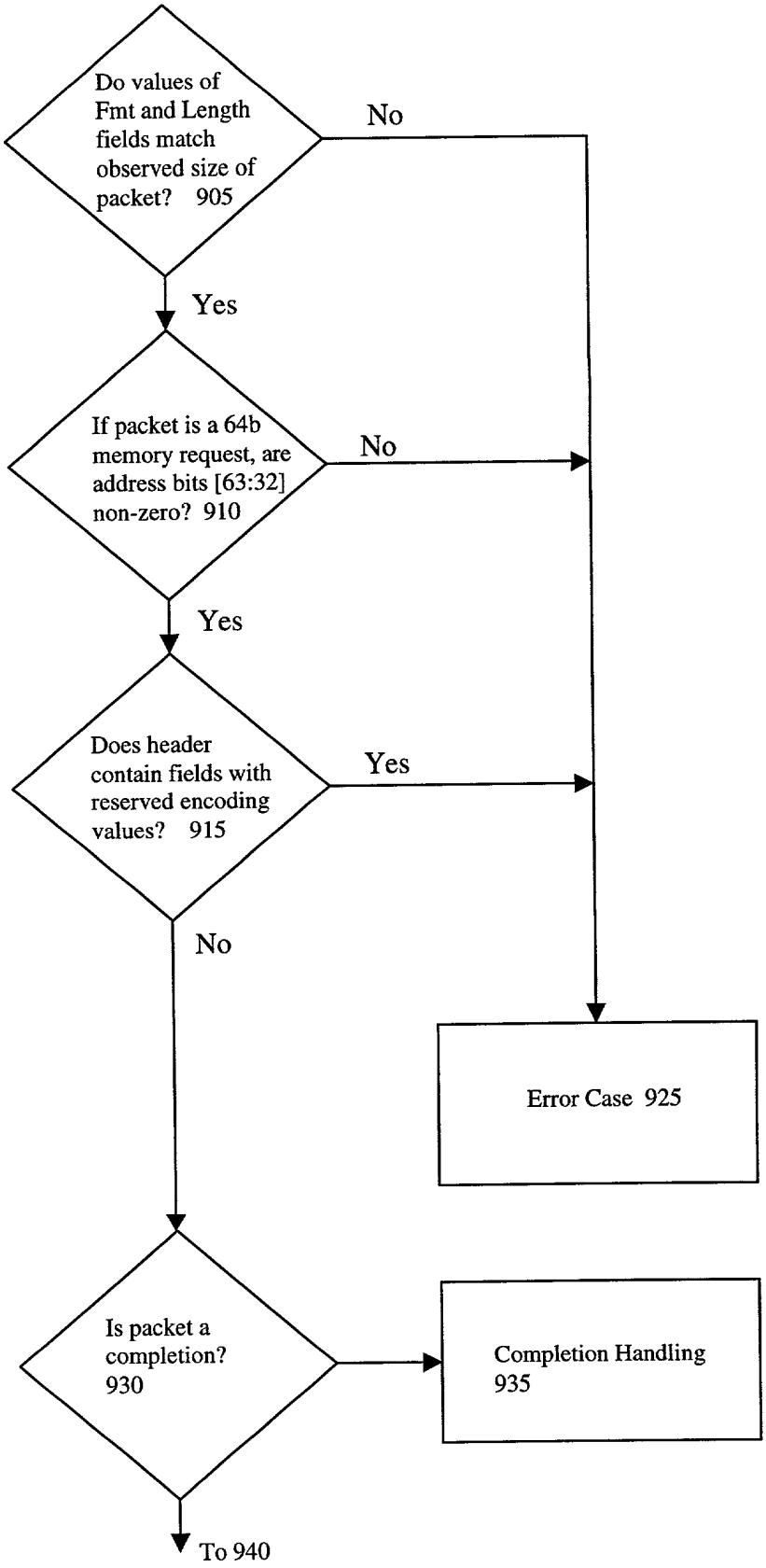
Do values of Fmt and Length fields match observed size of packet? 905
No
Yes
If packet is a 64b memory request, are address bits [63:32] non-zero? 910
No
Yes
Does header contain fields with reserved encoding values? 915
Yes
No
Error Case 925
Is packet a completion? 930
Completion Handling 935
To 940

Figure 9a

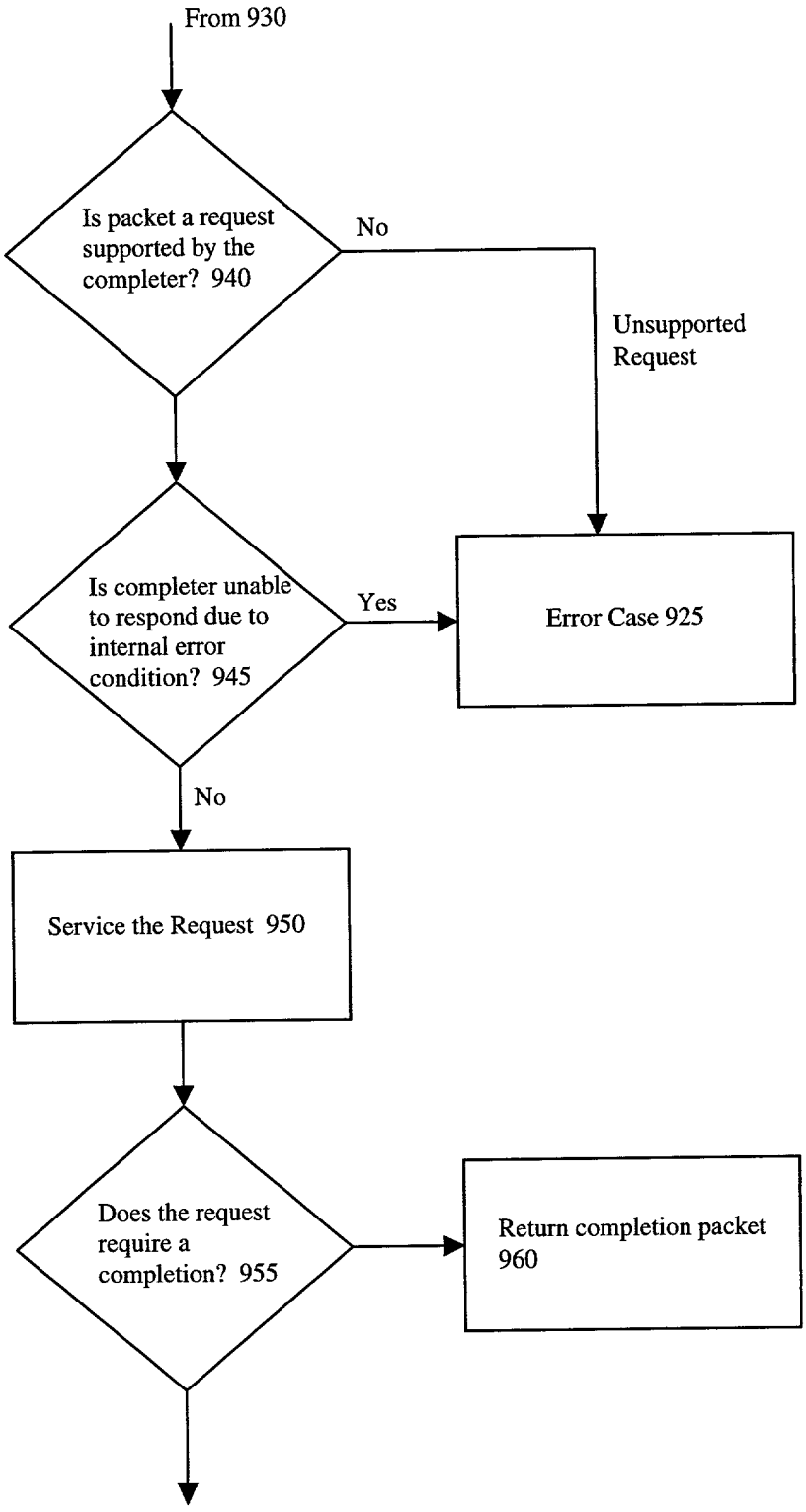
From 930
Is packet a request supported by the completer? 940
No
Unsupported Request
Is completer unable to respond due to internal error condition? 945
Yes
Error Case 925
No
Service the Request 950
Does the request require a completion? 955
Return completion packet 960
Done

Figure 9b

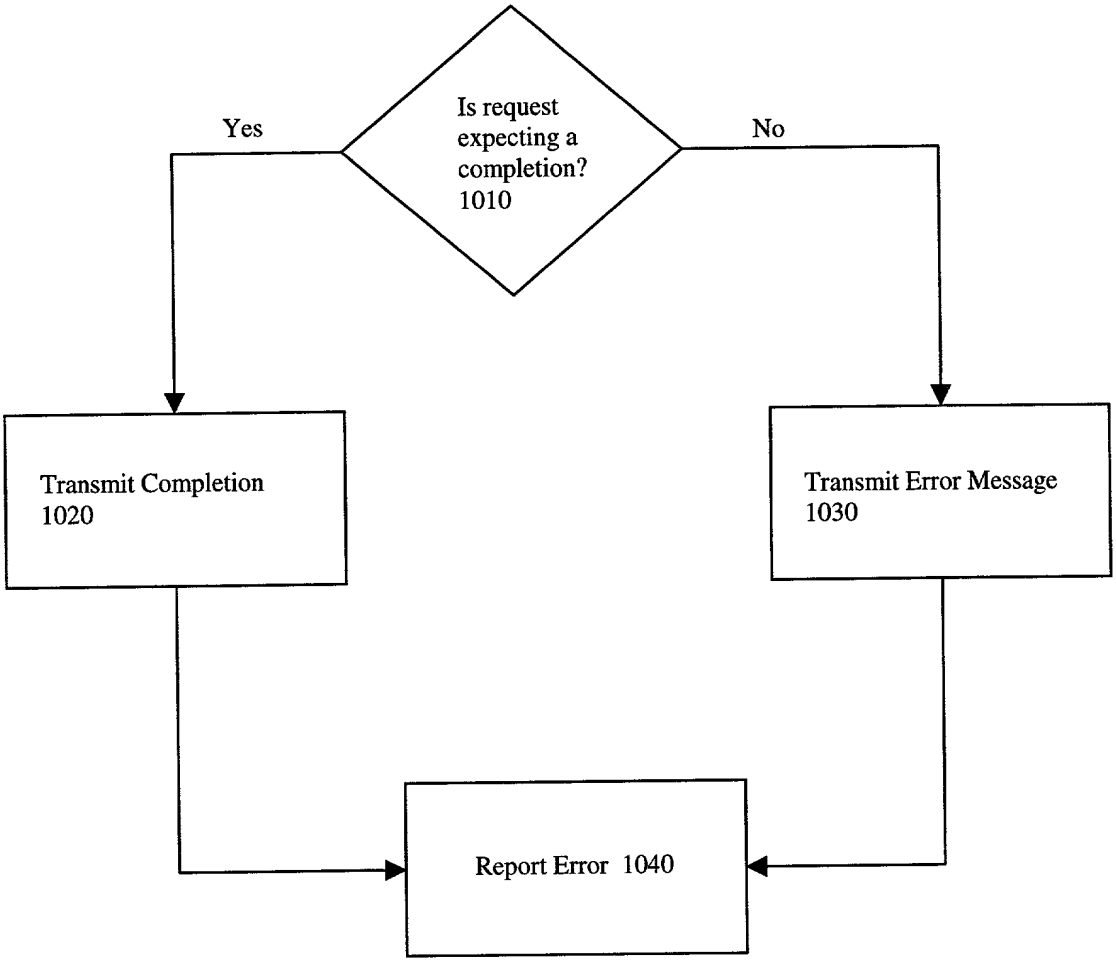
Is request expecting a completion? 1010
Yes
No
Transmit Completion 1020
Transmit Error Message 1030
Report Error 1040

Figure 10

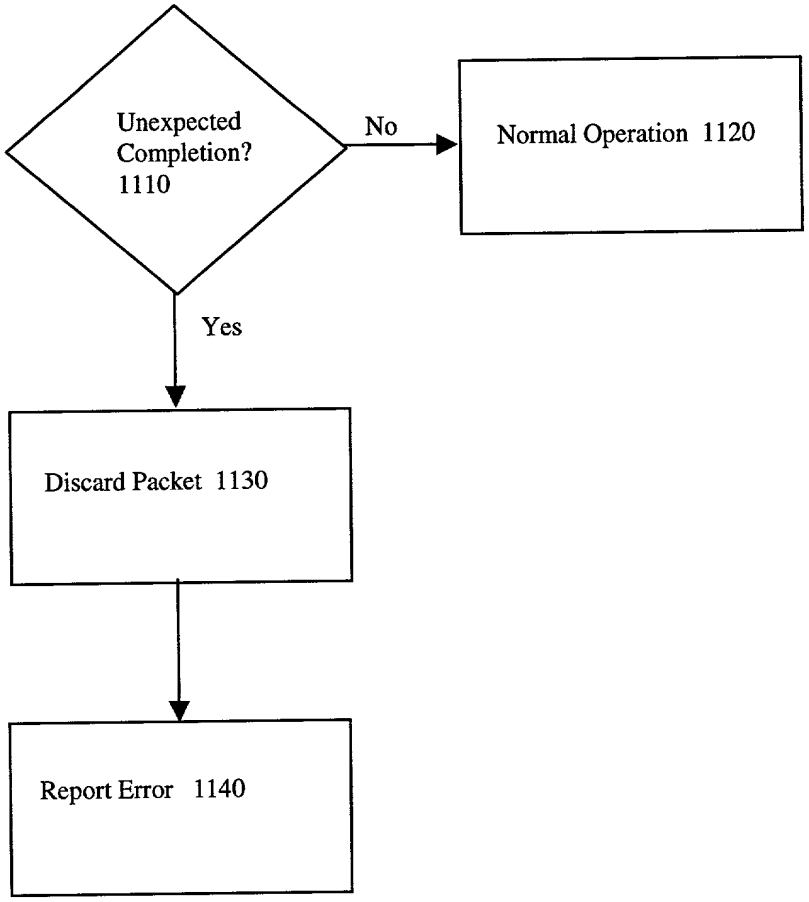
Unexpected Completion? 1110
No
Normal Operation 1120
Yes
Discard Packet 1130
Report Error 1140

Figure 11

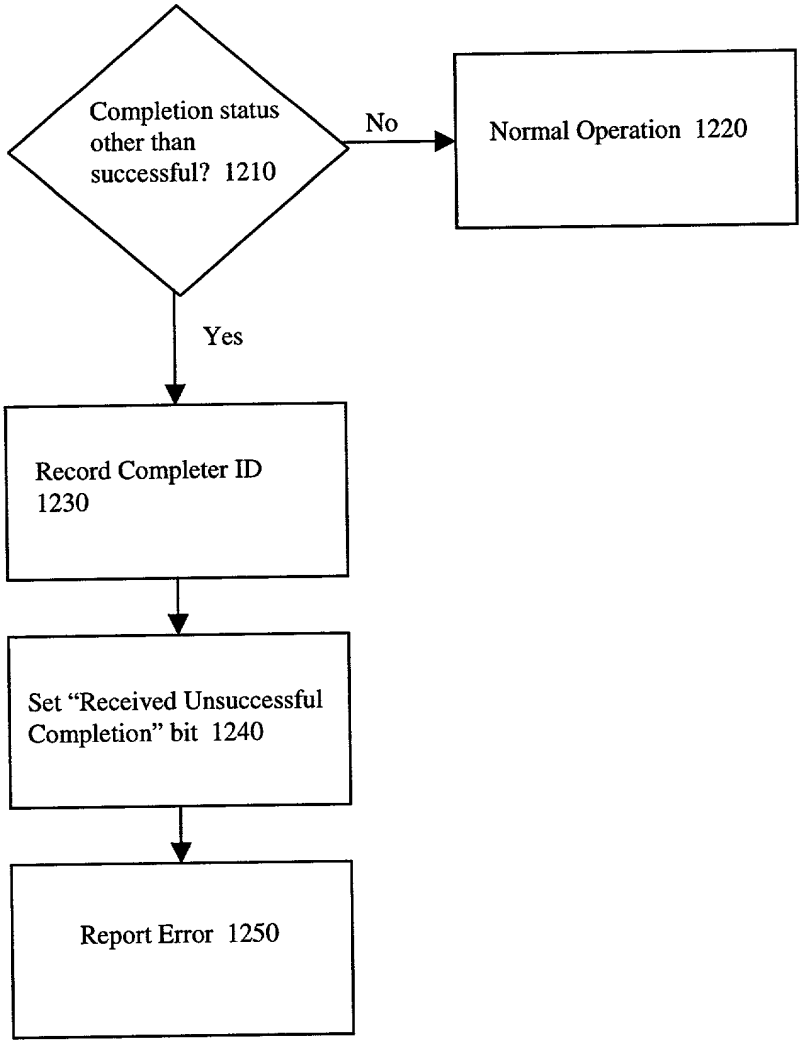
Completion status other than successful? 1210
No
Normal Operation 1220
Yes
Record Completer ID 1230
Set "Received Unsuccessful Completion" bit 1240
Report Error 1250

Figure 12

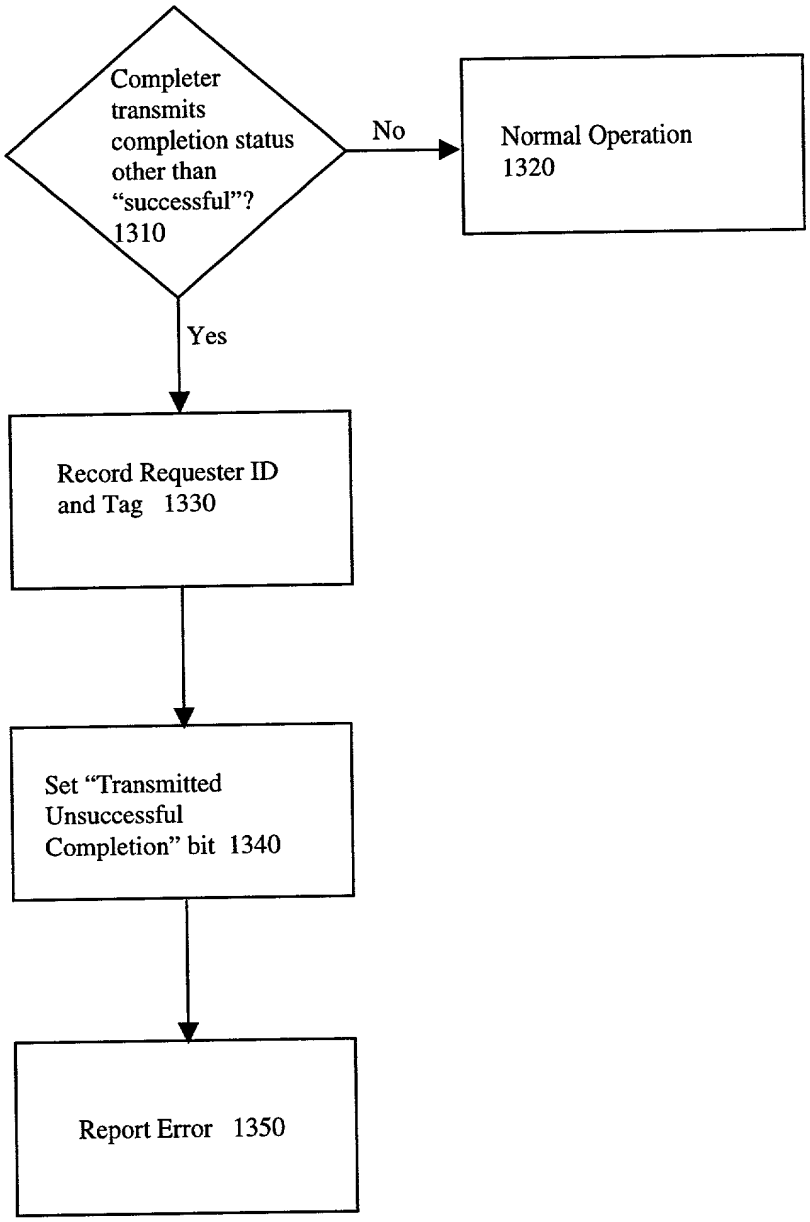
Completer transmits completion status other than "successful"? 1310
No
Normal Operation 1320
Yes
Record Requester ID and Tag 1330
Set "Transmitted Unsuccessful Completion" bit 1340
Report Error 1350

Figure 13